I HEREBY CERTIFY THIS PAPER OR FEE IS BEING DEPOSITED WITH THE U.S. POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 OFR 1.10 ON THE DATE INDICATED BELOW AND IS ADDRESSED TO: BOX PATHAT APPLICATIONS, ASSISTANT APPLICATIONS, ASSISTANT APPLICATIONS, THE PROPERTY OF Case No. 00CT16653315 DIRECTOR U.S. PATENT AND TRADEMARK OFFICE WASHINGTON, DC 20231 20231. EXPRESS MAIL NO: EL747059609US smitted herewith for filing is the patent DATE OF DEPOSIT: November 5, 2001 application of: Jennifer Ferguson 

SIGNATURE:

Mors: For:

Michele ALIA Michele CARRANC Carmello PISTRITTO

POWER DOWN PROTOCOL FOR INTEGRATED CIRCUITS

## Enclosed are:

XXXXXX

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Patent Application: 19 pages, 11 claims is enclos 5 sheets of drawings is enclosed. The suggested drawing figure to be published is FIG. 4. Citation Under 37 CFR 1.97 and PTO-1449 is enclosed. A Preliminary Amendment is enclosed. Submission of Proposed Drawing Modification. 11 claims is enclosed. Assignee info: Name STMicroelectronics S.r.I. Via C. Olivetti, 2, 20041 Agrate Brianza, Italy Address:

Country of Incorporation: Italy

Applicant claims priority benefit to the following foreign application(s): Country: Application No.: Filing Date: Europe 00830731.6

The Declaration and Filing Fee are NOT ENCLOSED.

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